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APPLICATION NO.	APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR		DRNEY DOCKET NO.	CONFIRMATION NO.	
10/534,962 05/16/2005		Masayoshi Kondo	033036.088 1696				
TITLE OF INVENTION PRODUCING MULTIL	: CIRCUIT BOARD, M AYER WIRING BOAR	ULTI-LAYER WIRING DS	BOARDS, METHOD OF			<u> </u>	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE		
nonprovisional	NO	\$1510	\$300	\$0	\$1810	07/15/2009	
EXAMINER		ART UNIT	CLASS-SUBCLASS				
PATEL, ISHWARBHAI B 2841		- 2841 .	174-254000				
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nterest as shown by the	161	tates Patent and Tradema	rk Office.	July 1	3, 2009		
vped or printed na	Robert G	G. Weilacher		Registration No.	20531		

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